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Fig.1

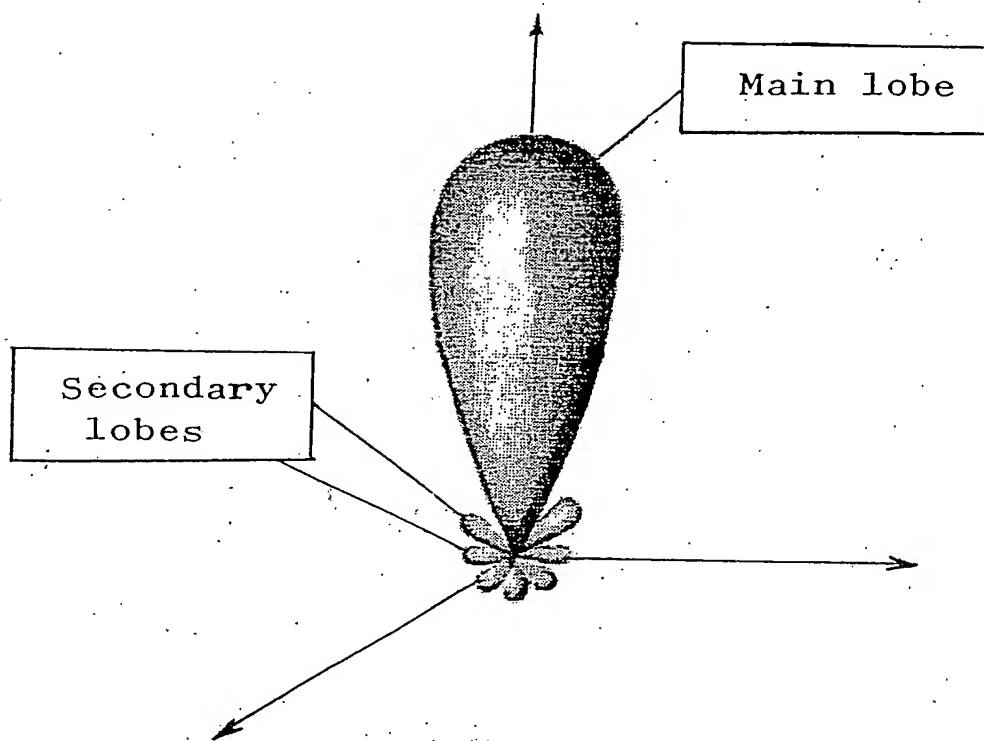
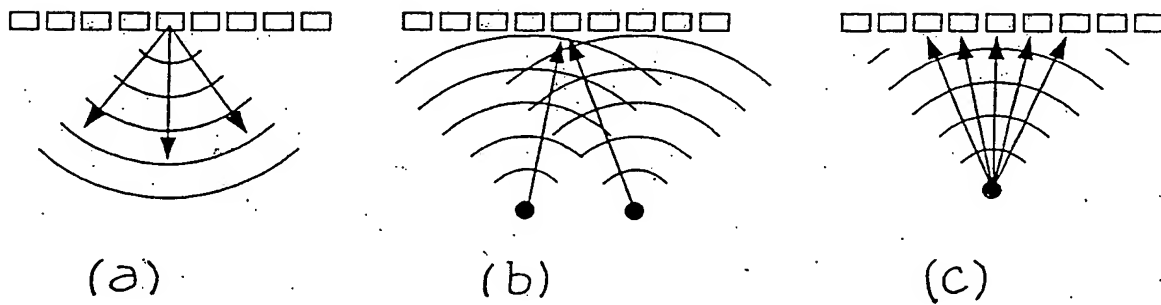


Fig.2



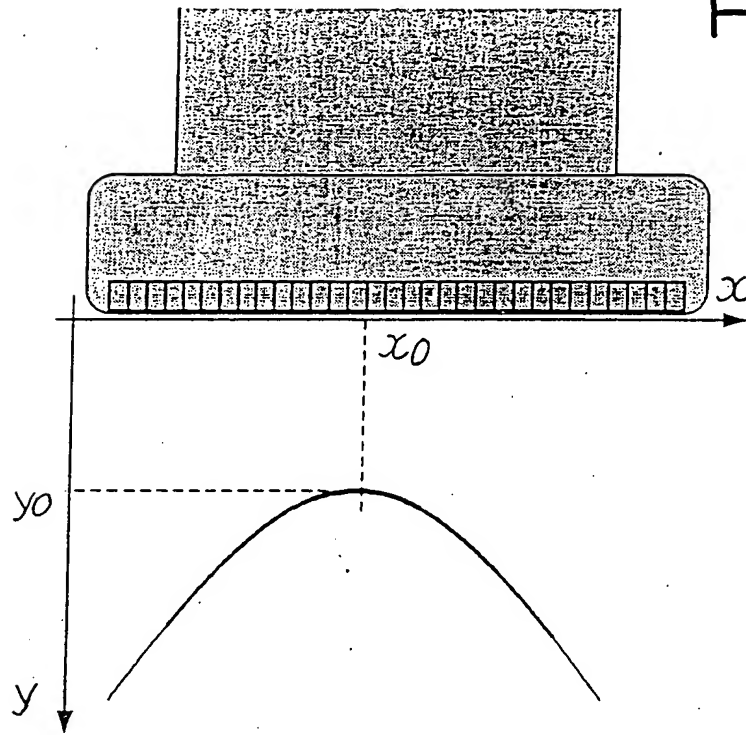


Fig. 3

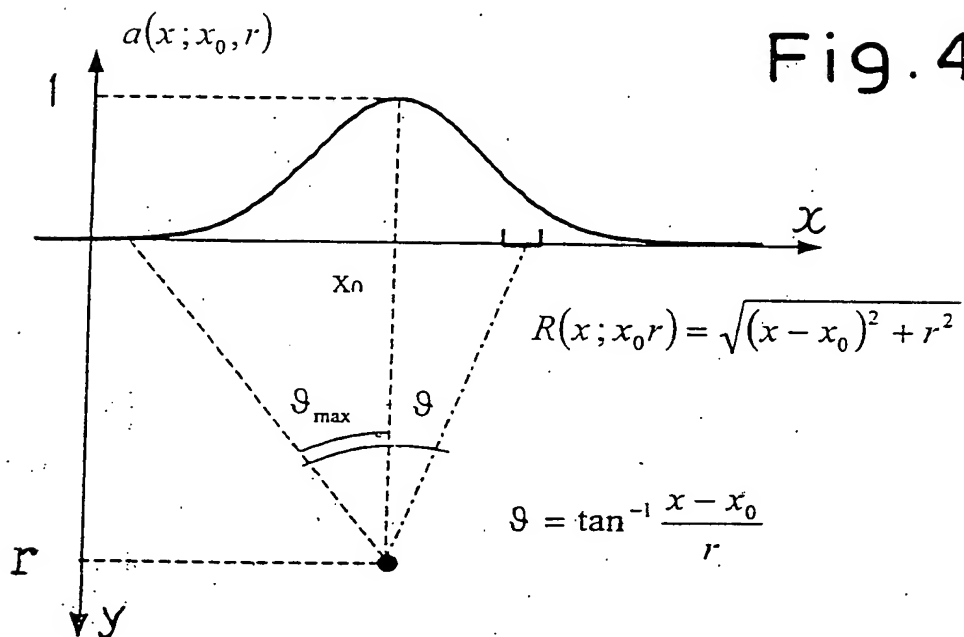
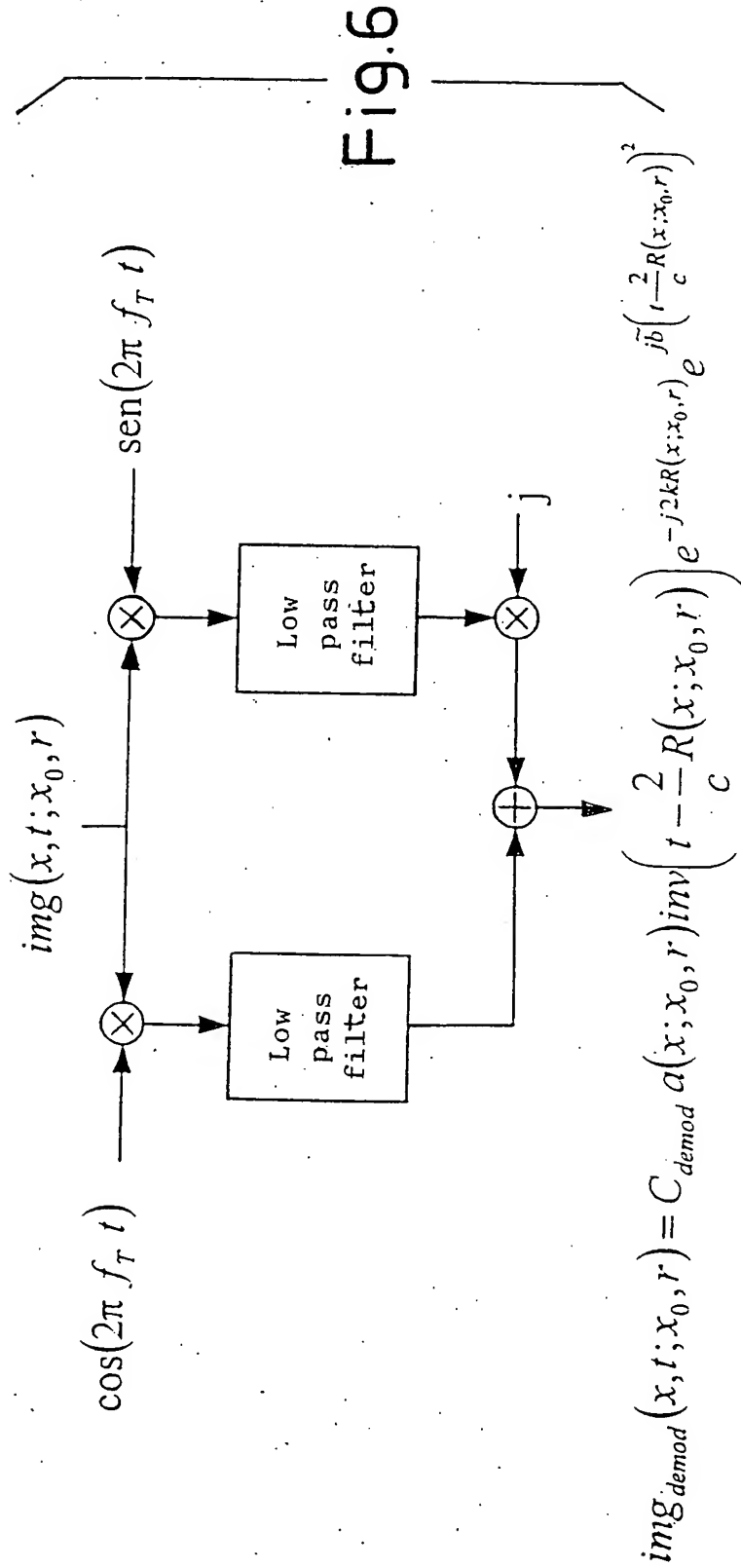
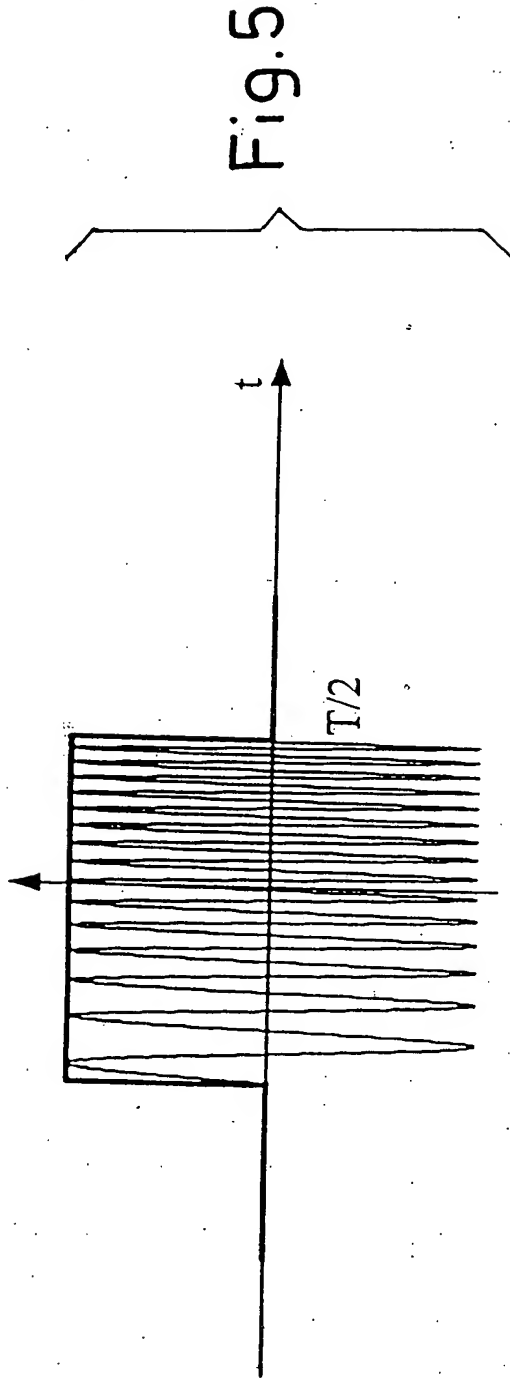


Fig. 4



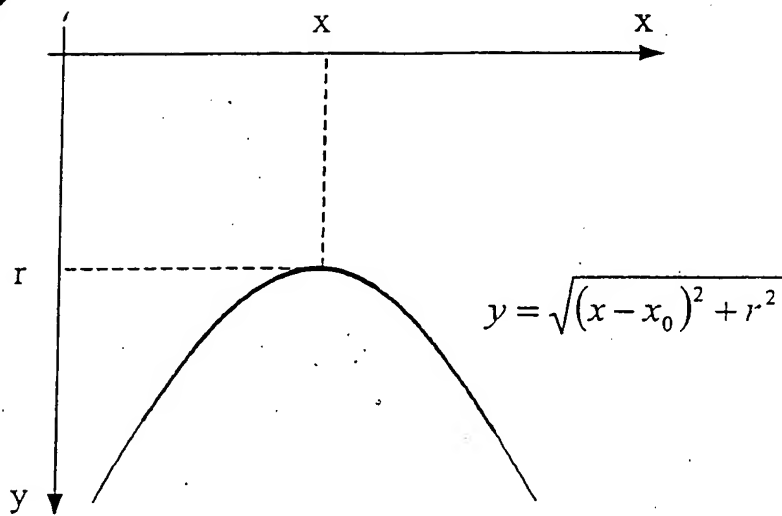


Fig.7

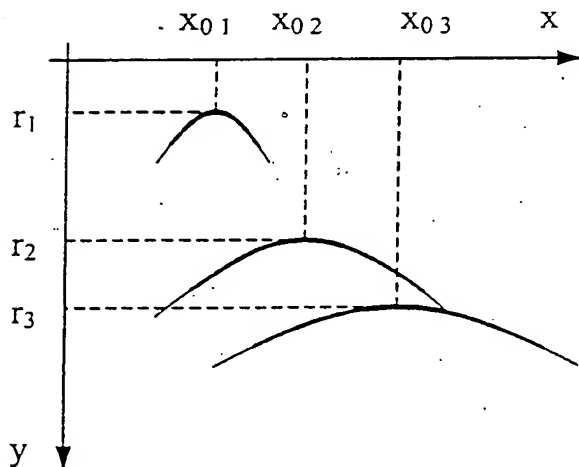


Fig.8

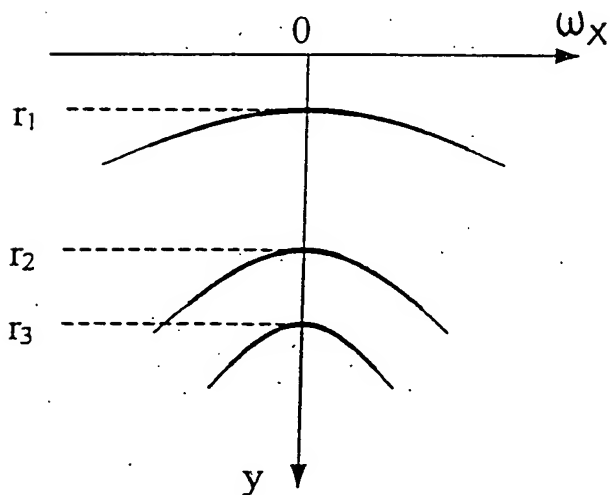


Fig.9



5 / 24

Fig.10

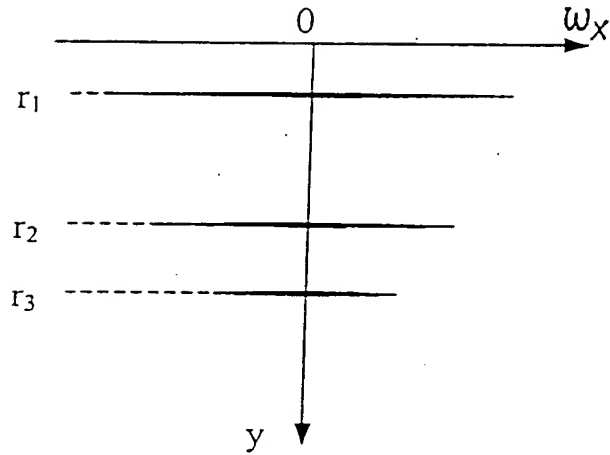
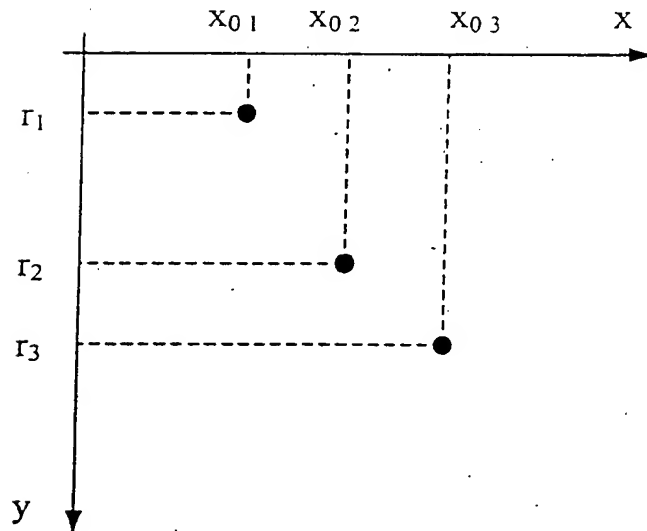


Fig.11





6 / 24

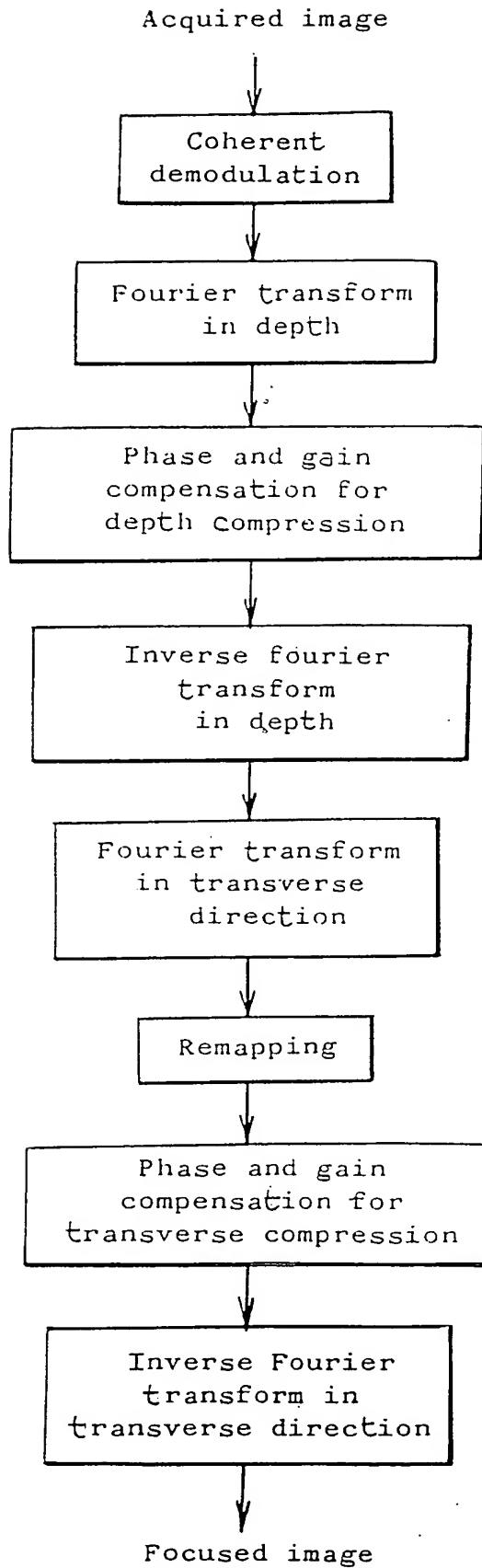
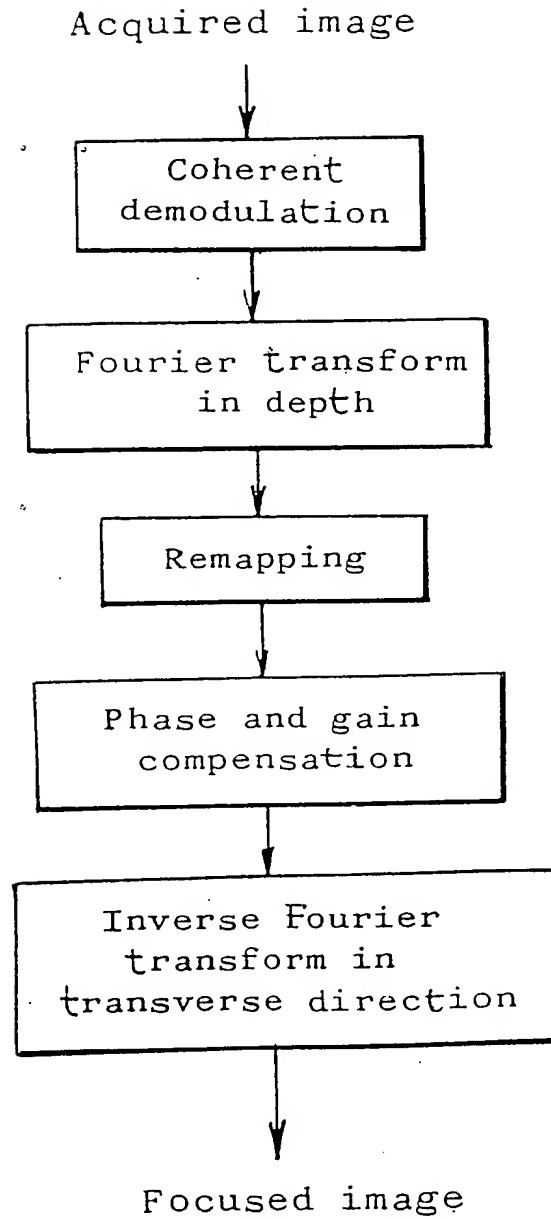


Fig.12



7 / 24

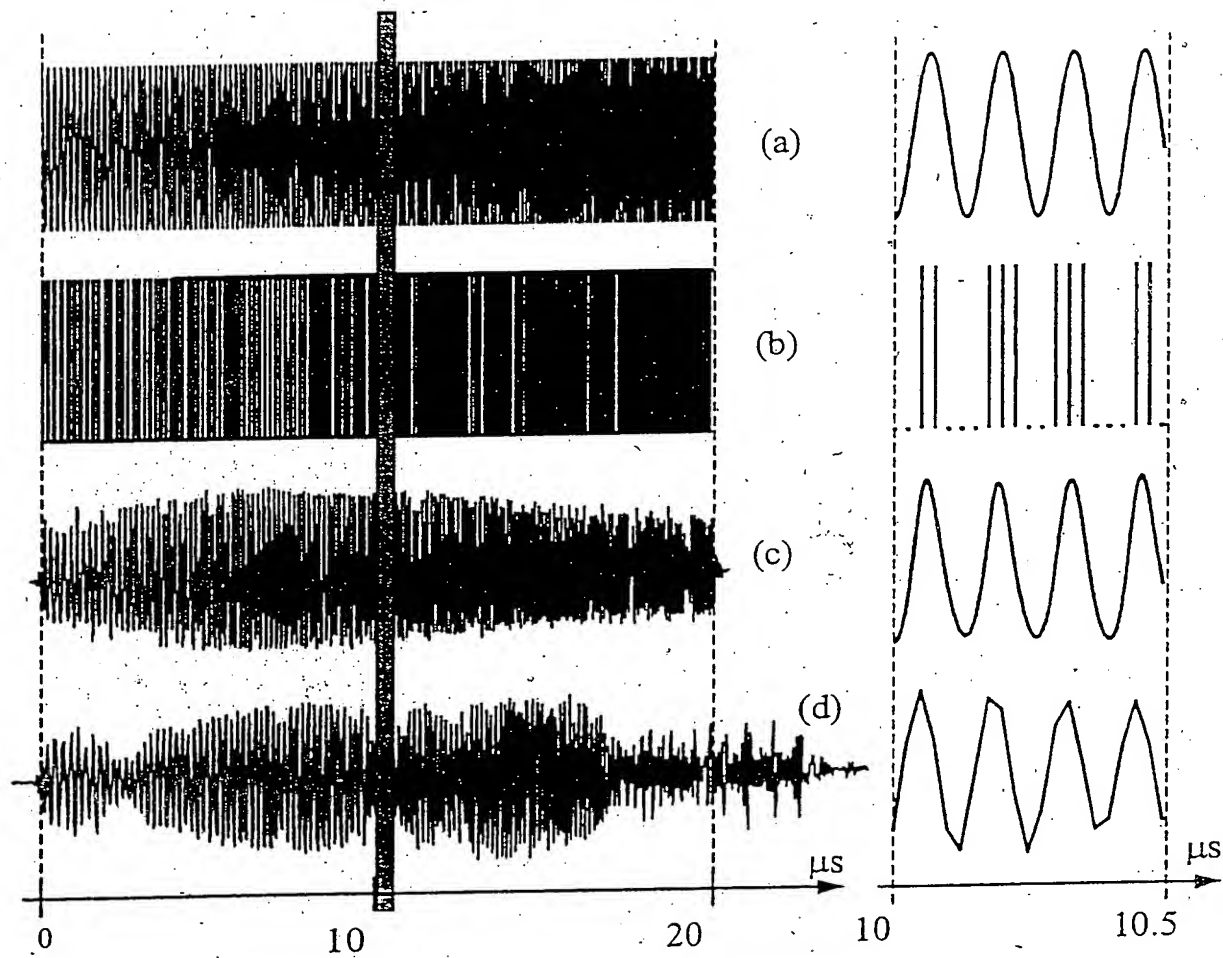
Fig.13





8 / 24

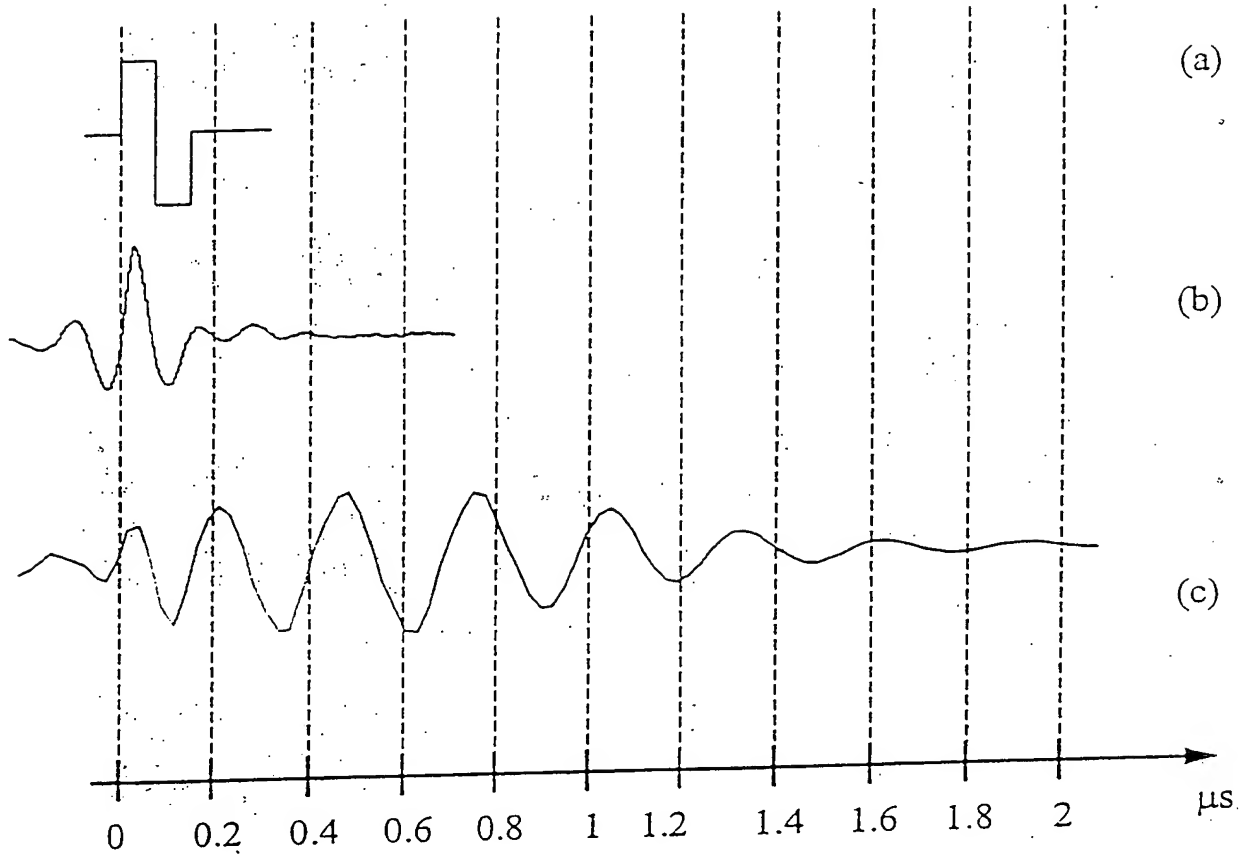
Fig.14





9/24

Fig.15



10 / 24

Fig.16

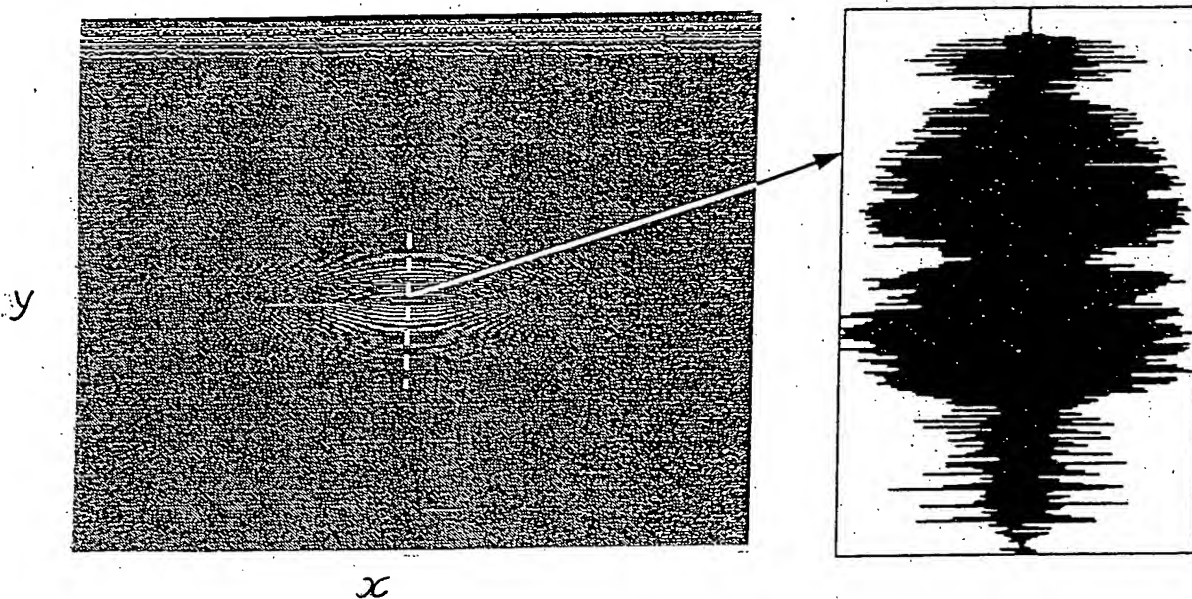


Fig.17

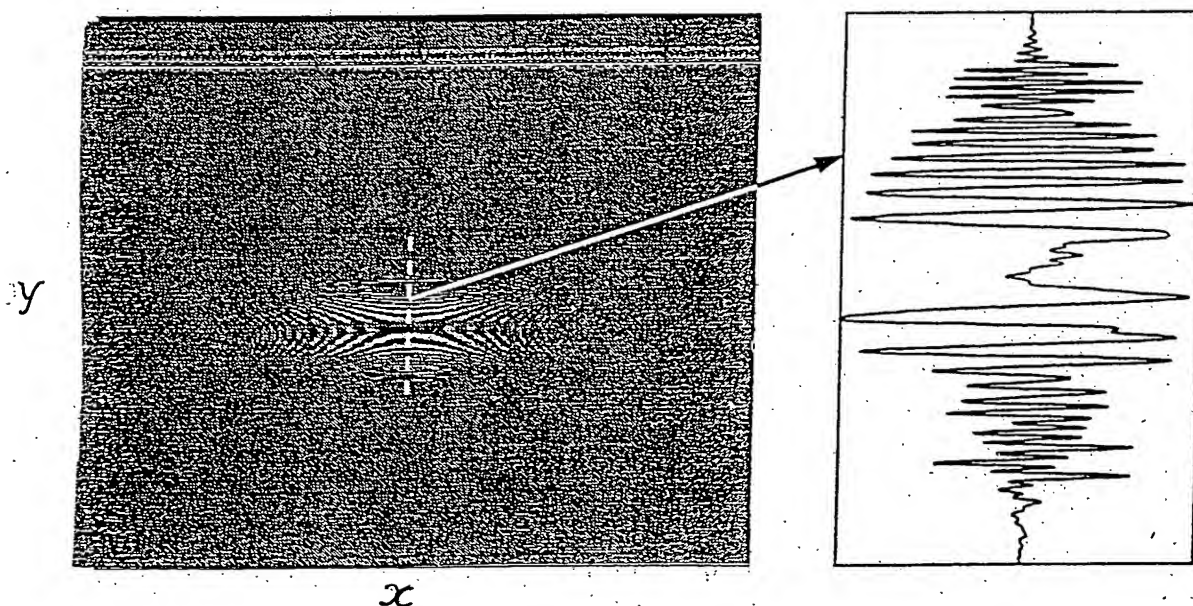
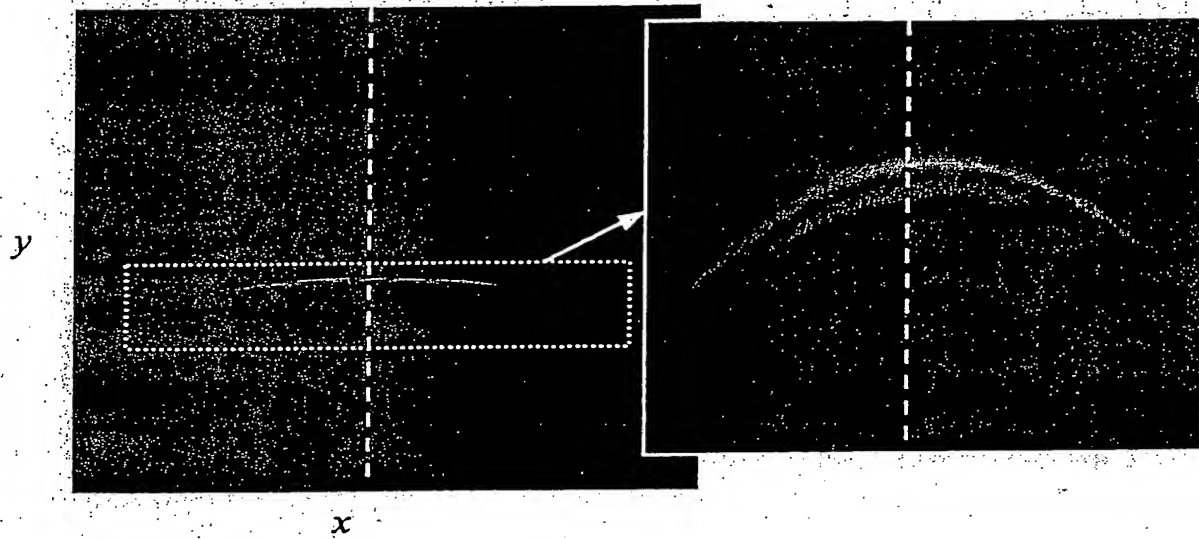


Fig.18

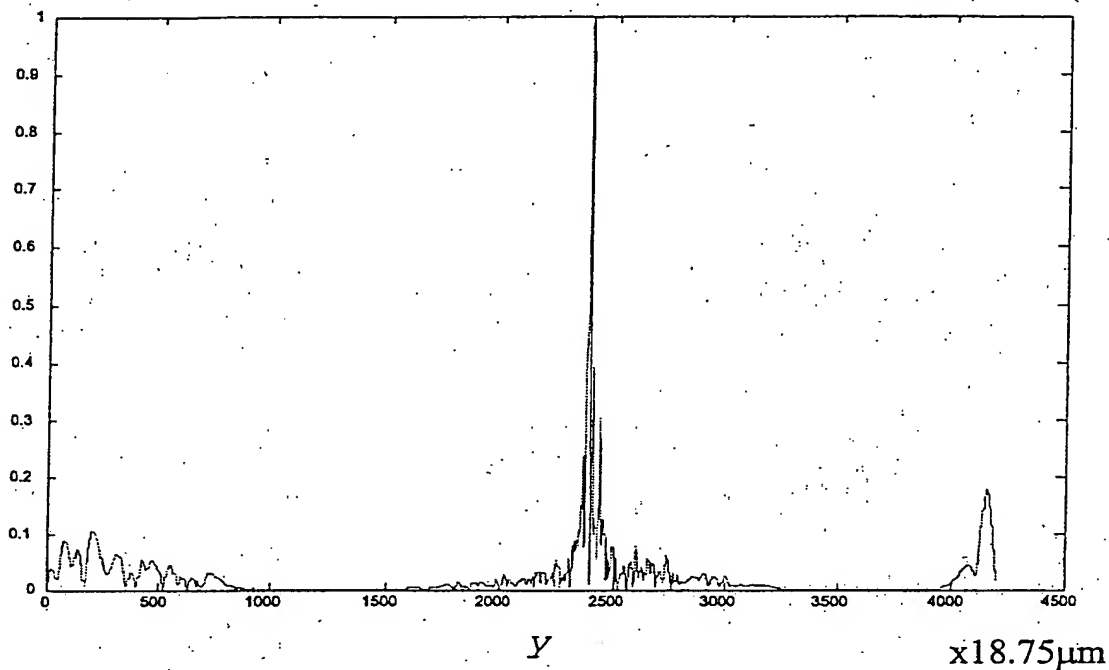




12 / 24

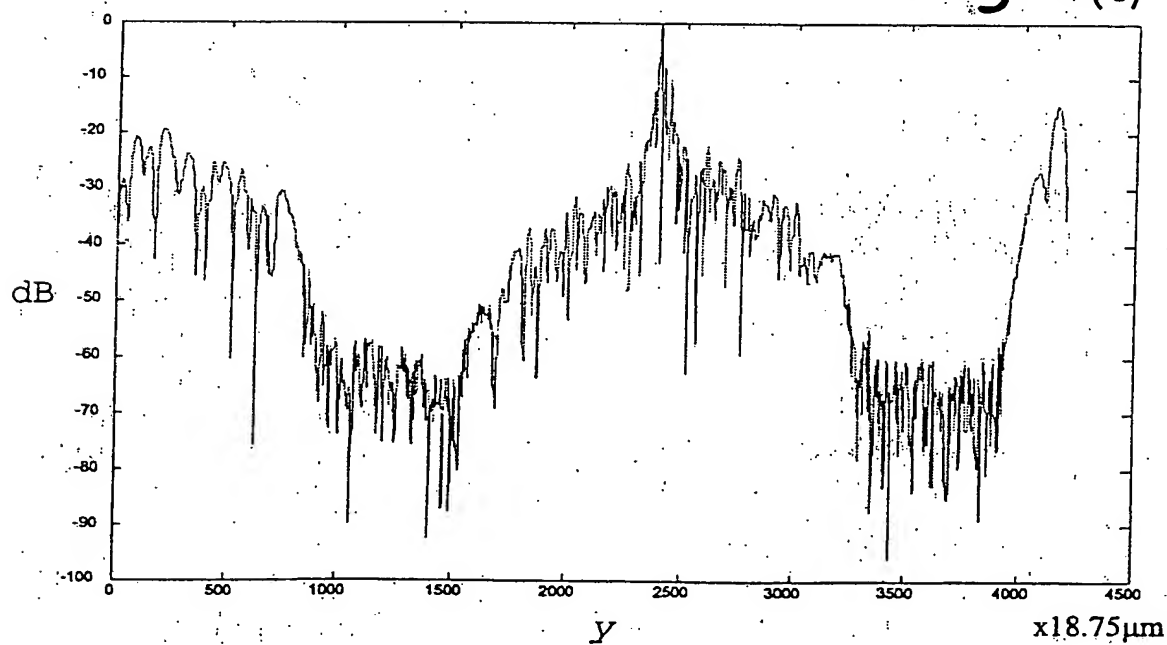
Normalized
module

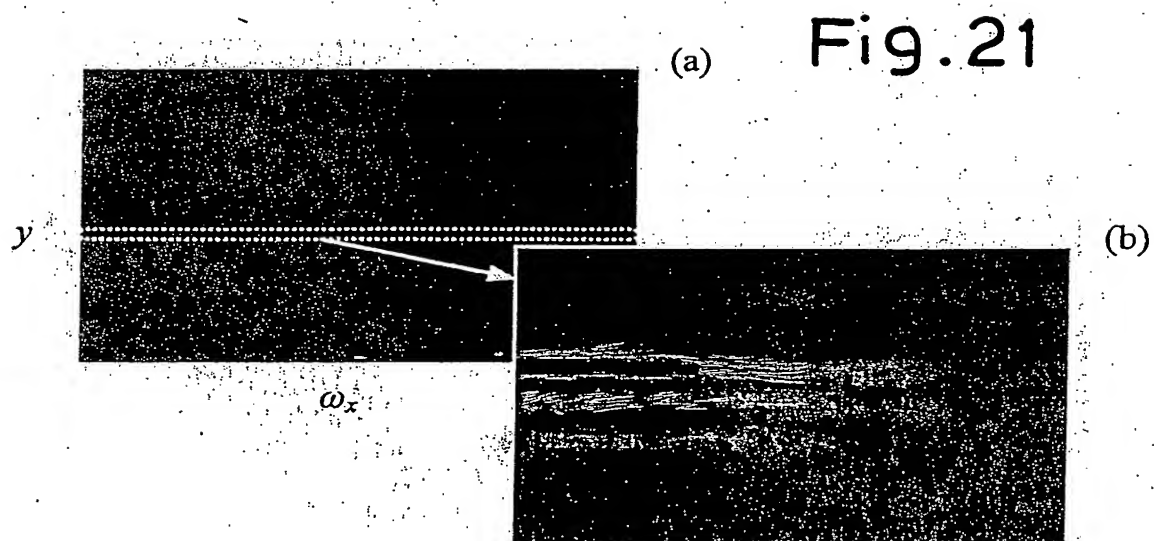
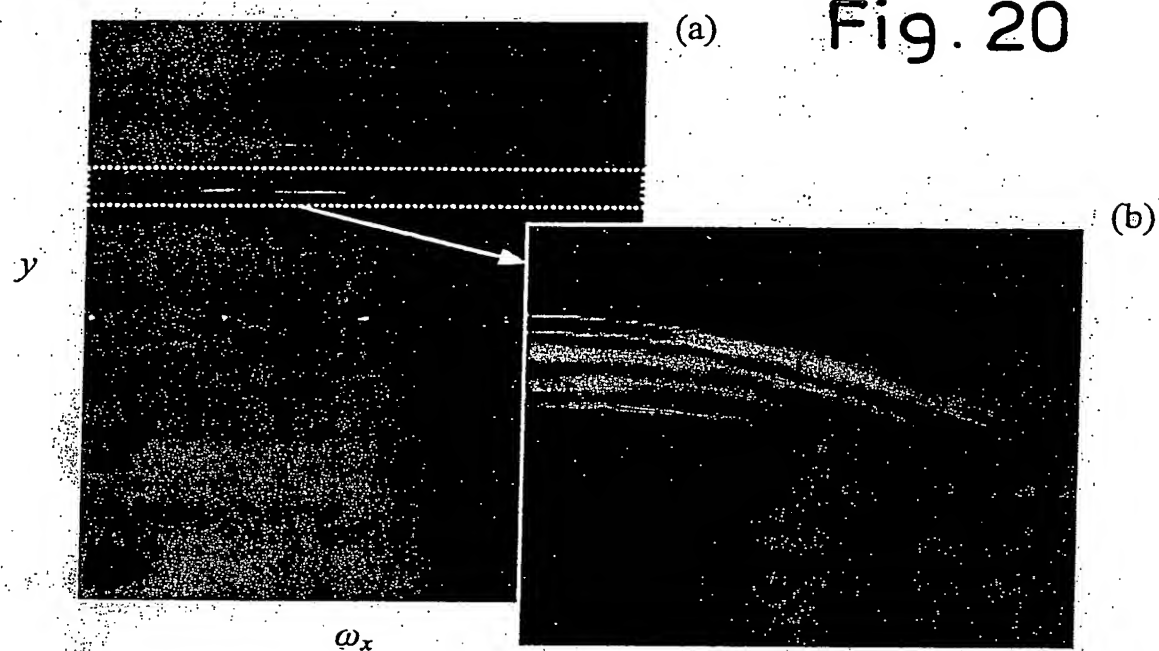
Fig.19(a)



Normalized
module

Fig.19(b)







14 / 24

Fig.22(a)

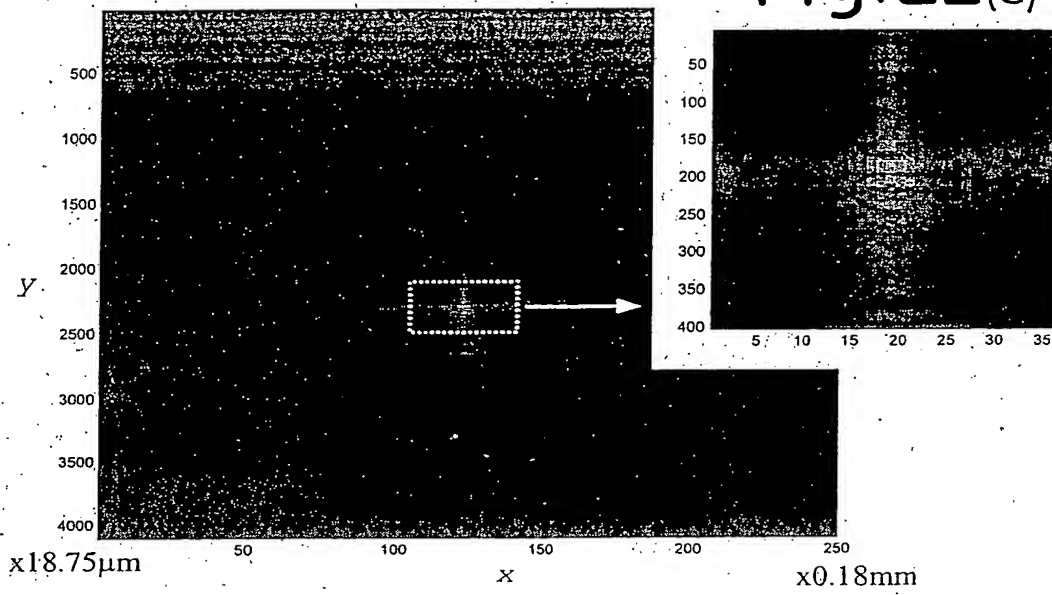
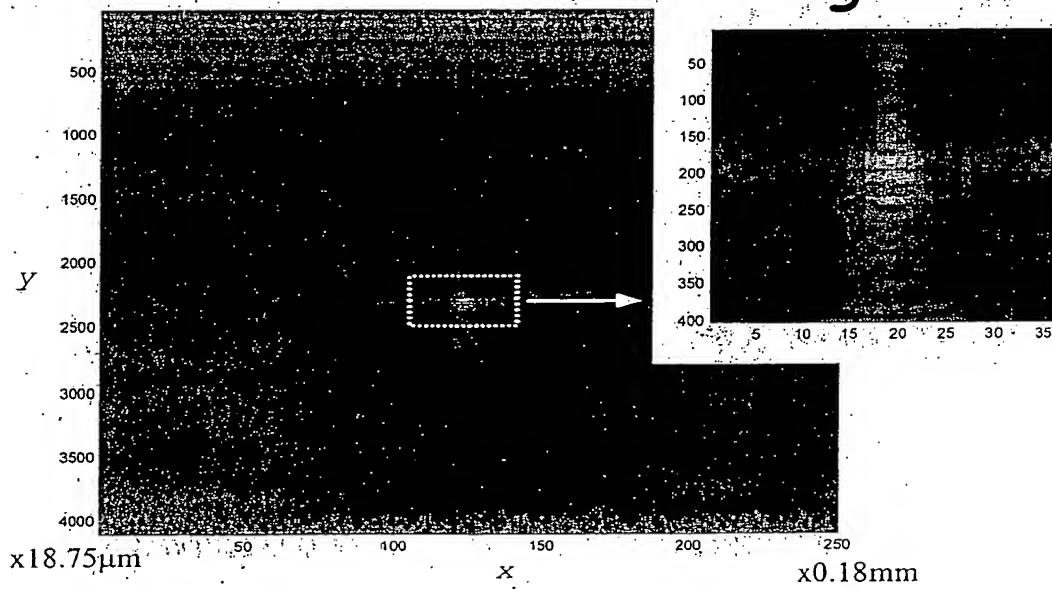


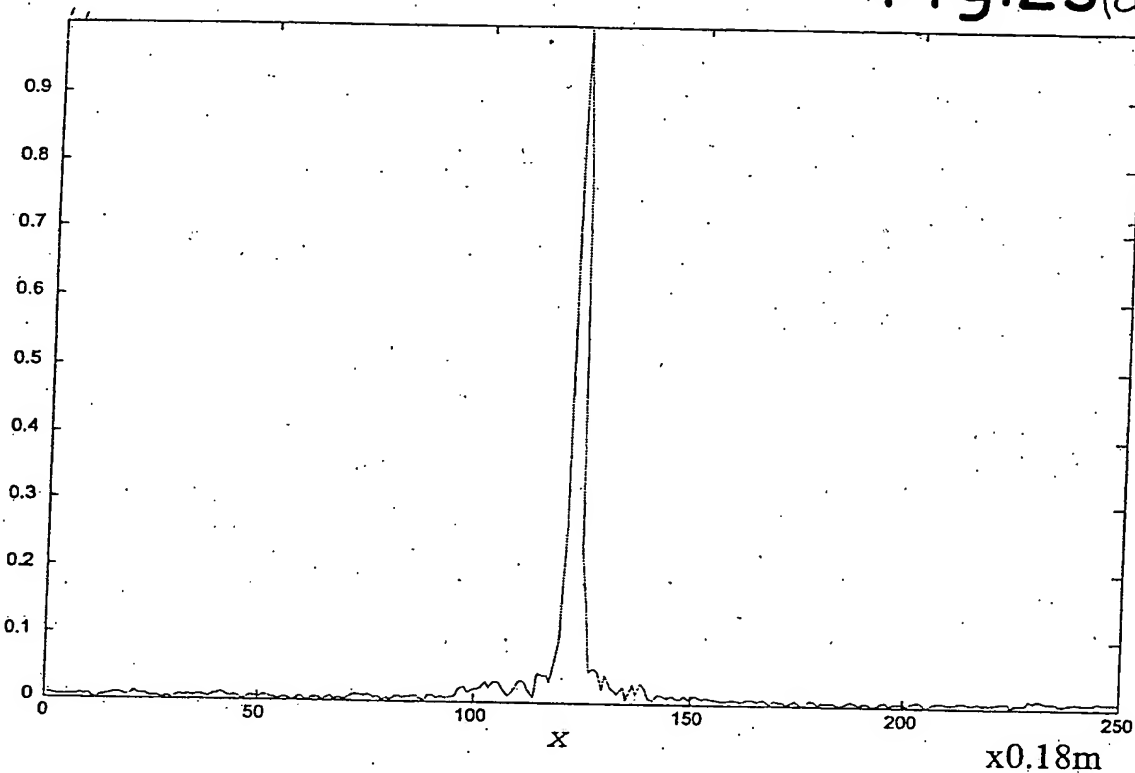
Fig.22(b)





15 / 24

Fig.23(a)



Normalized module

Fig.23(b)

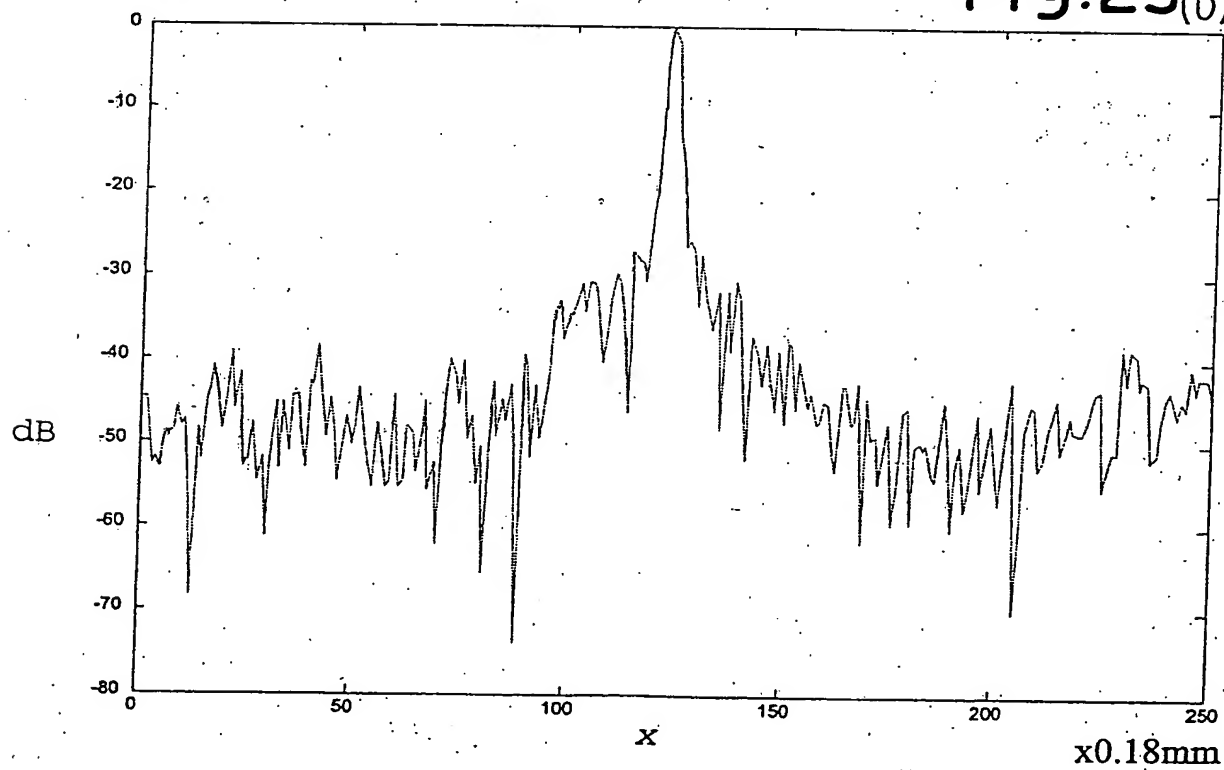


Fig. 24

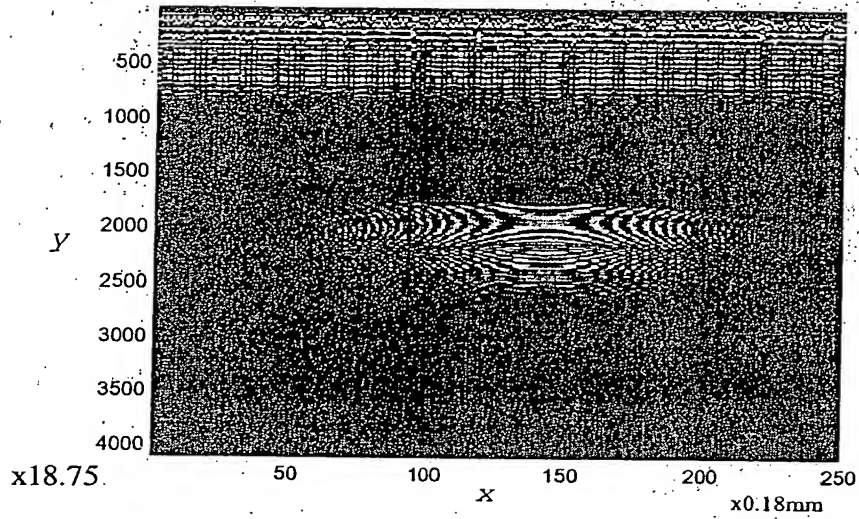
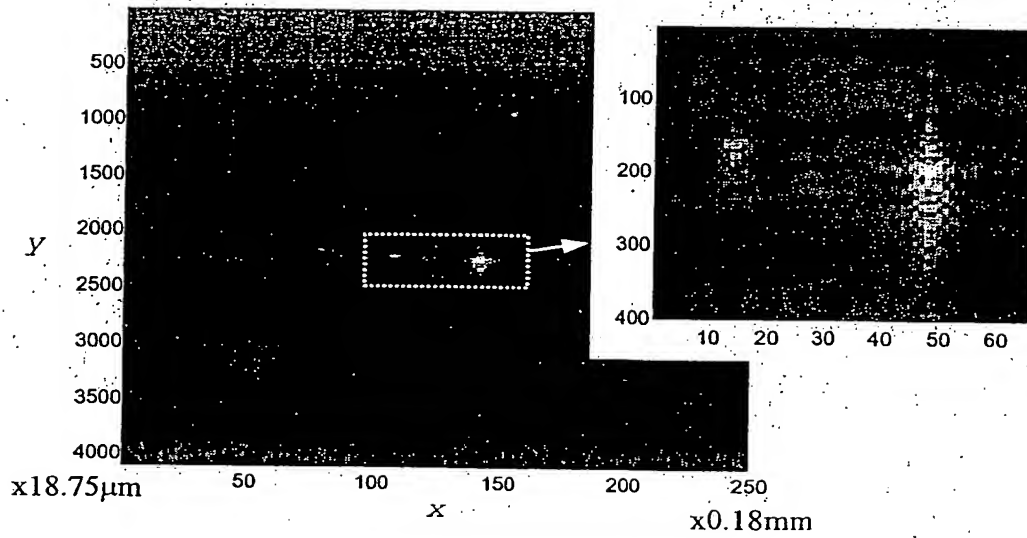


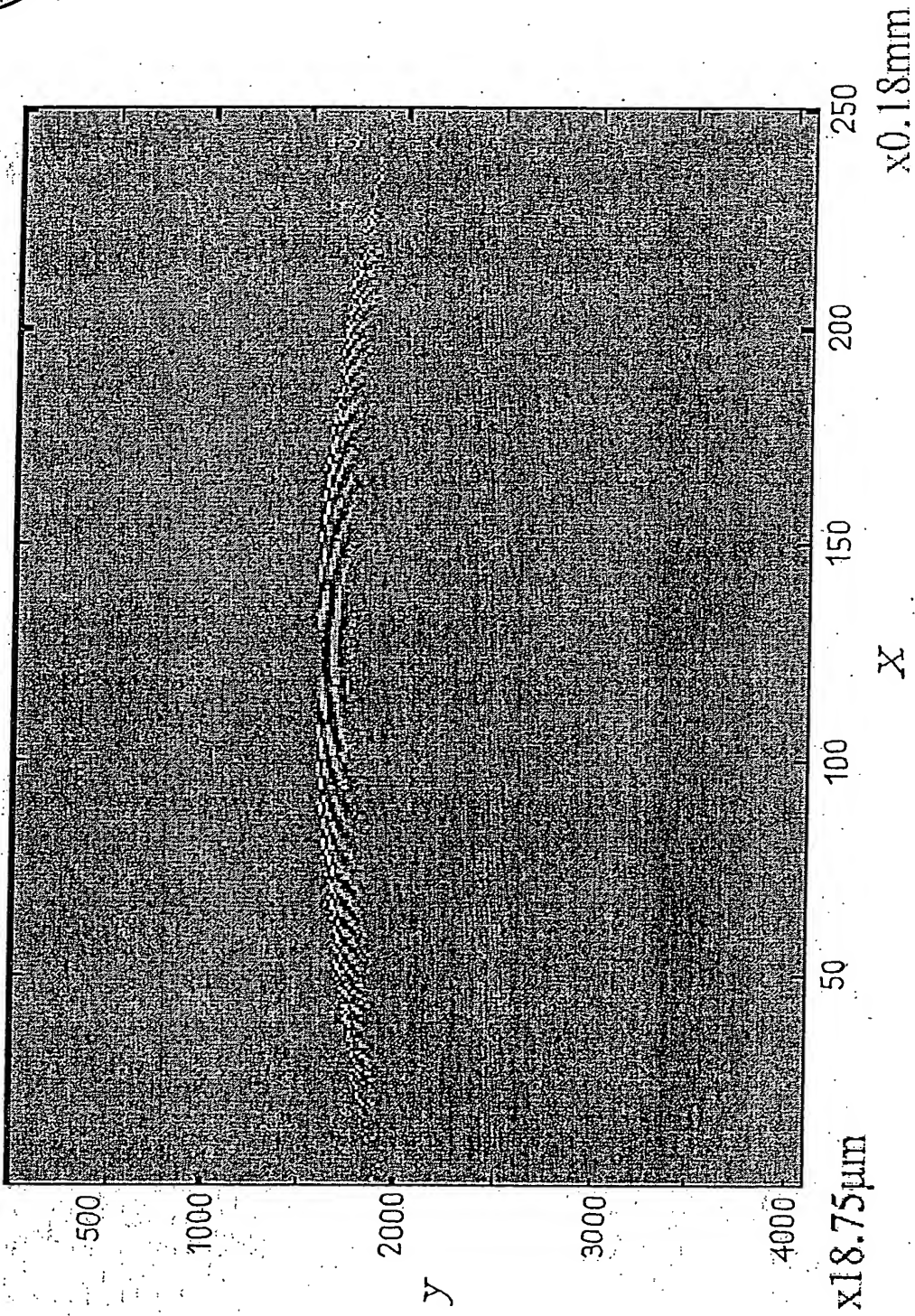
Fig 25





17 / 24

Fig.26





18 / 24

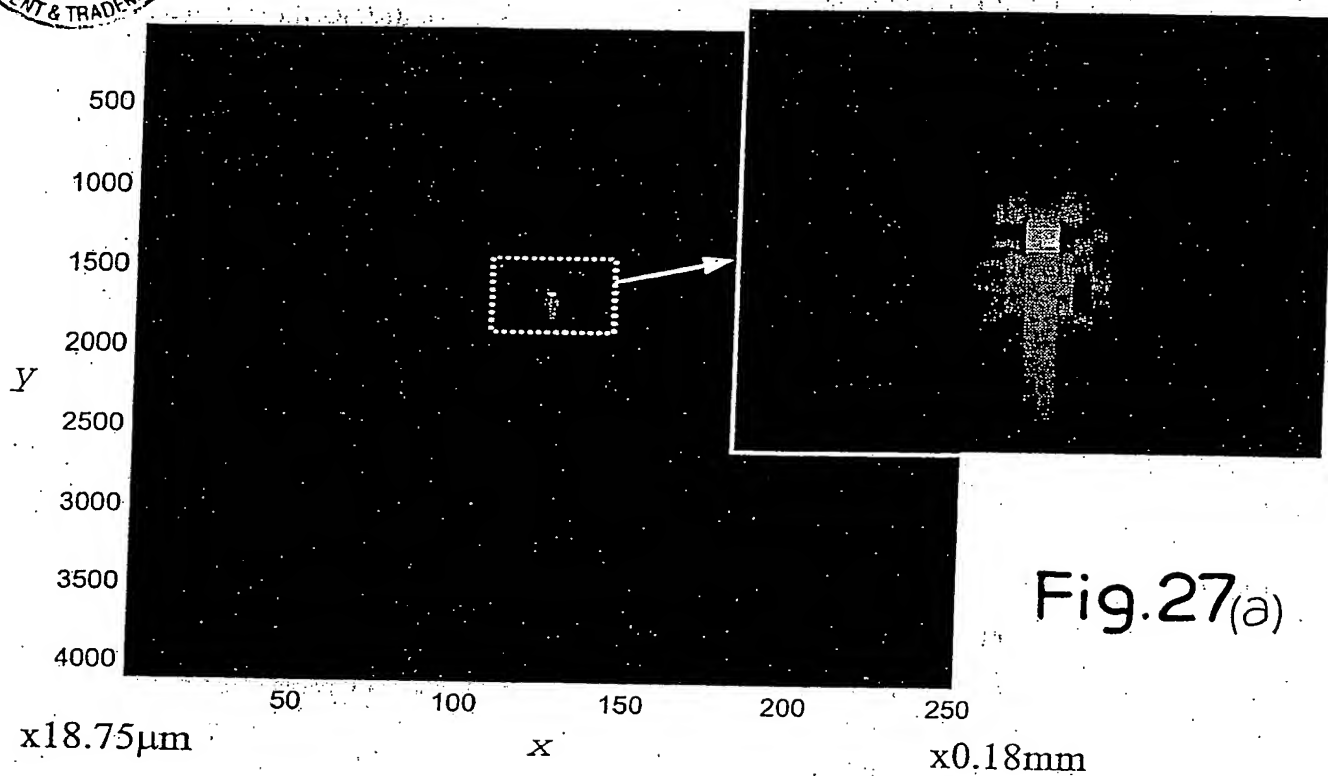
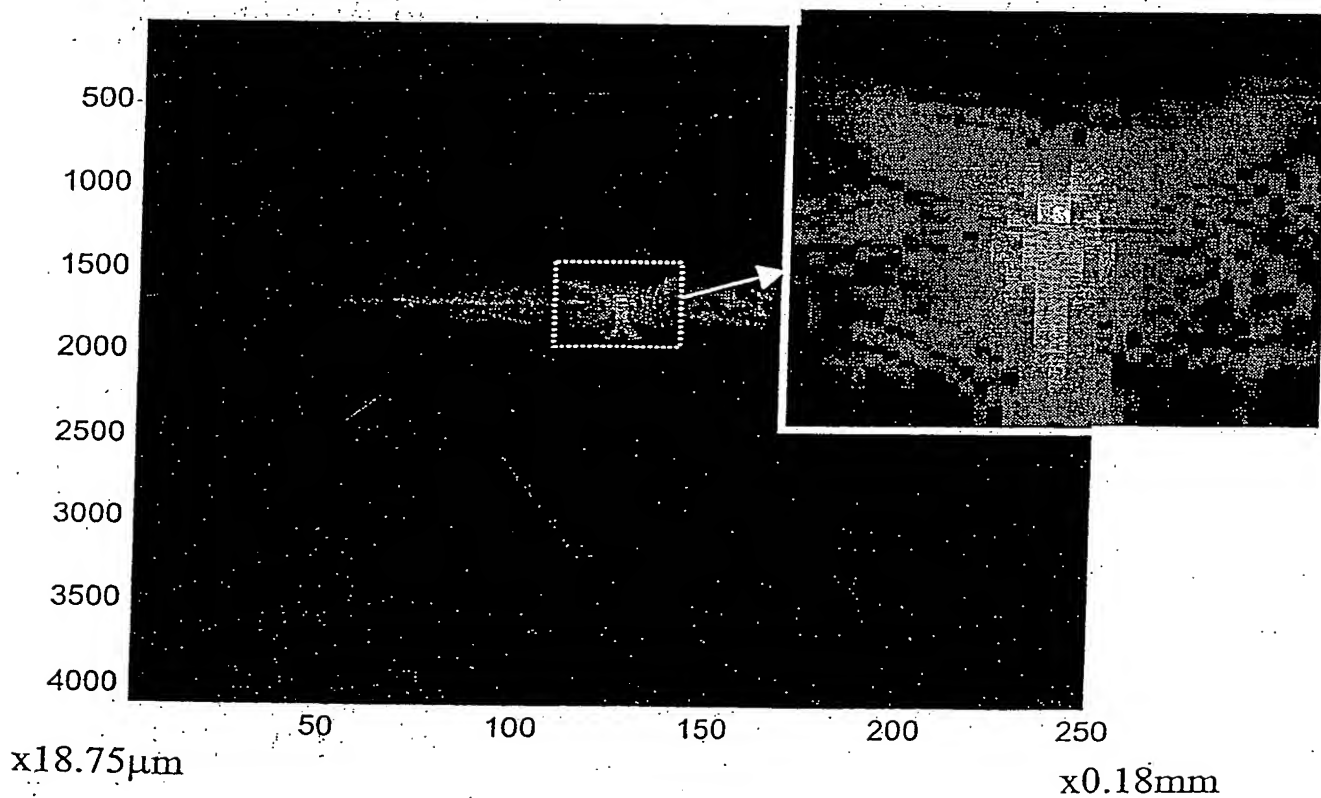
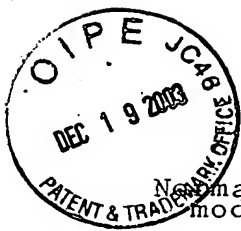


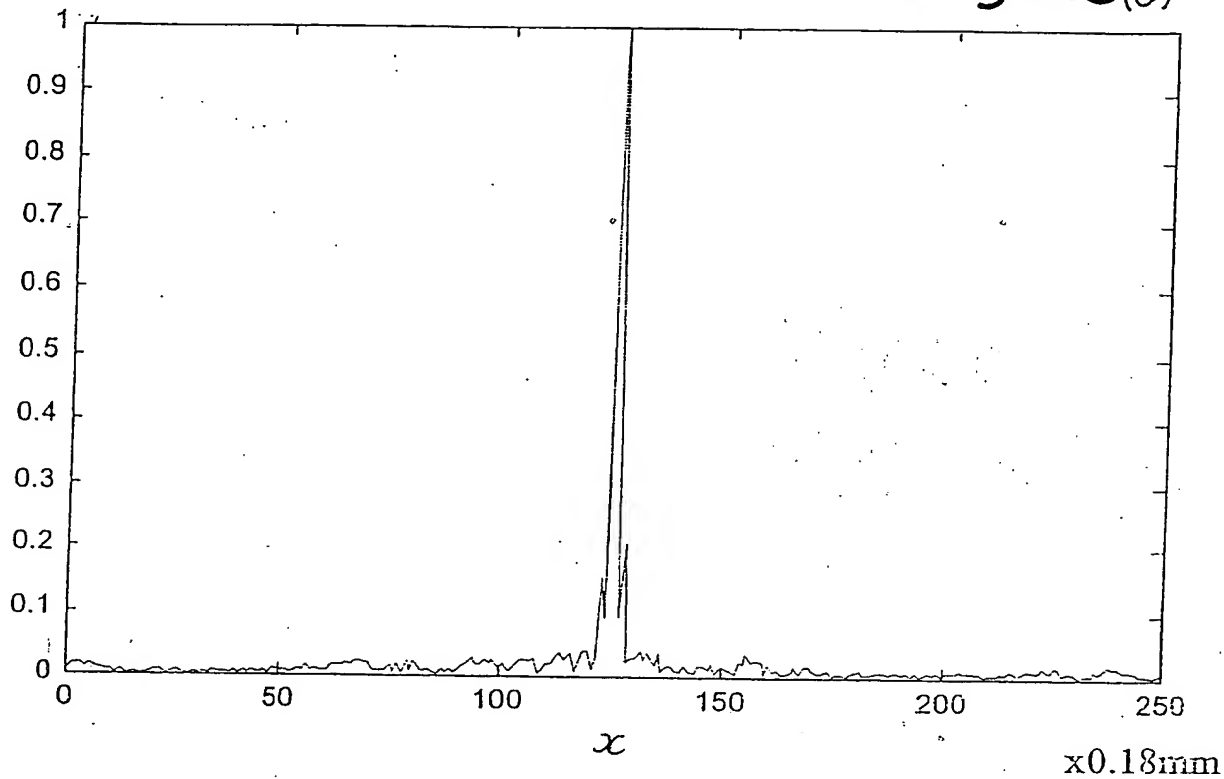
Fig.27(b)





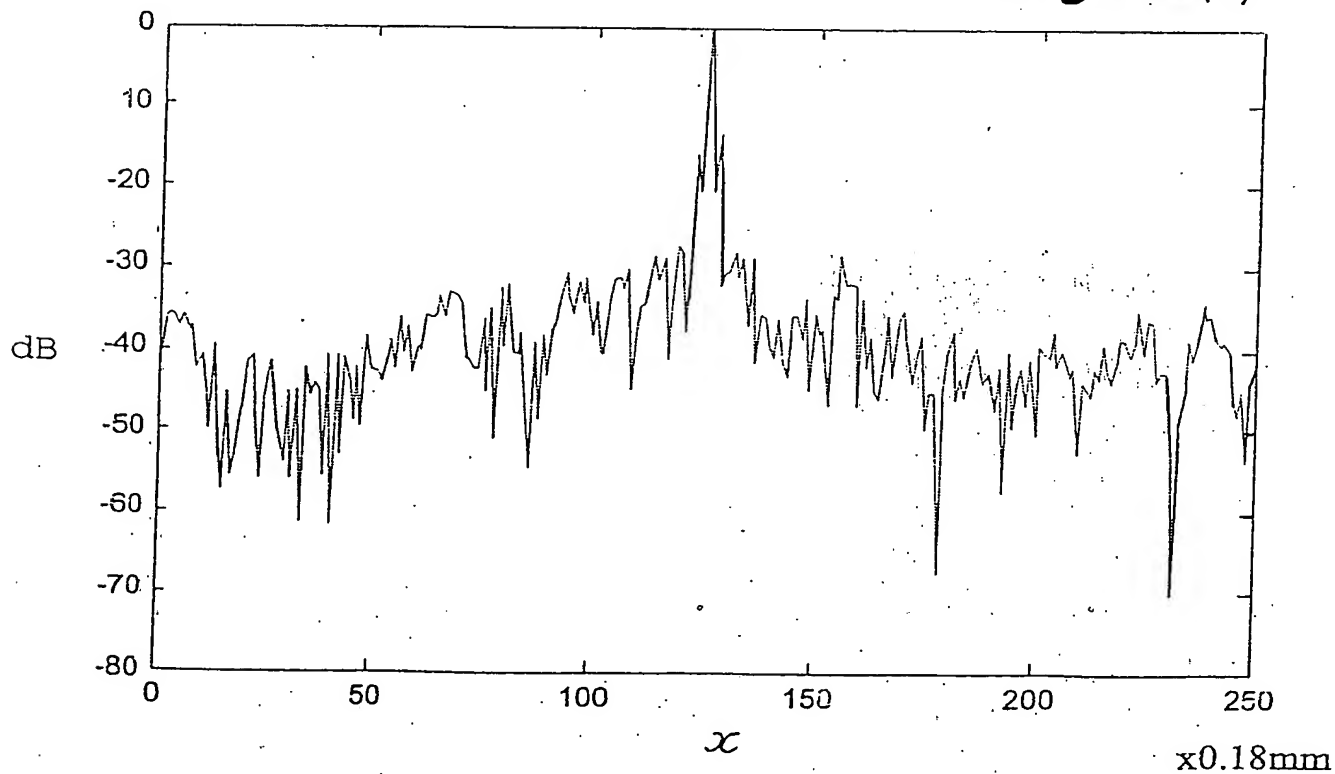
19 / 24

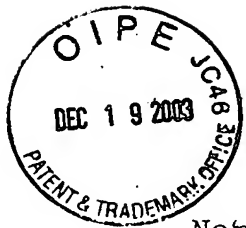
Fig.28(a)



Normalized module

Fig.28(b)

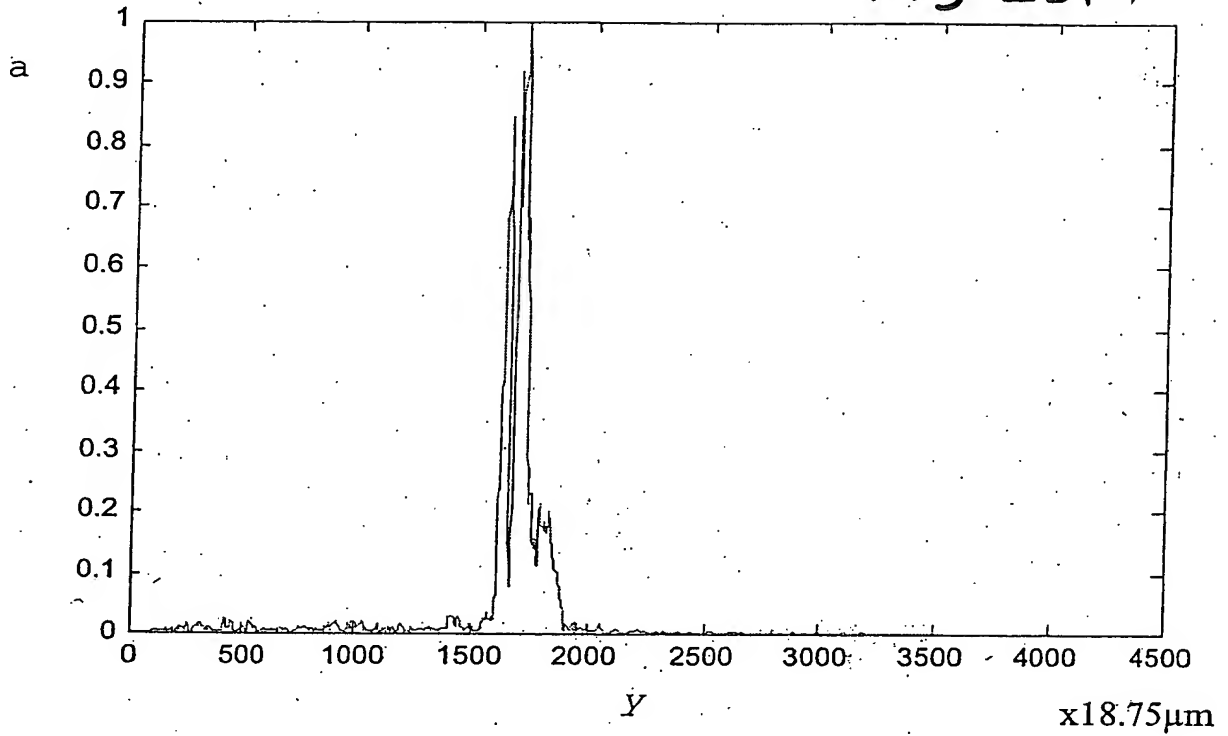




20 / 24

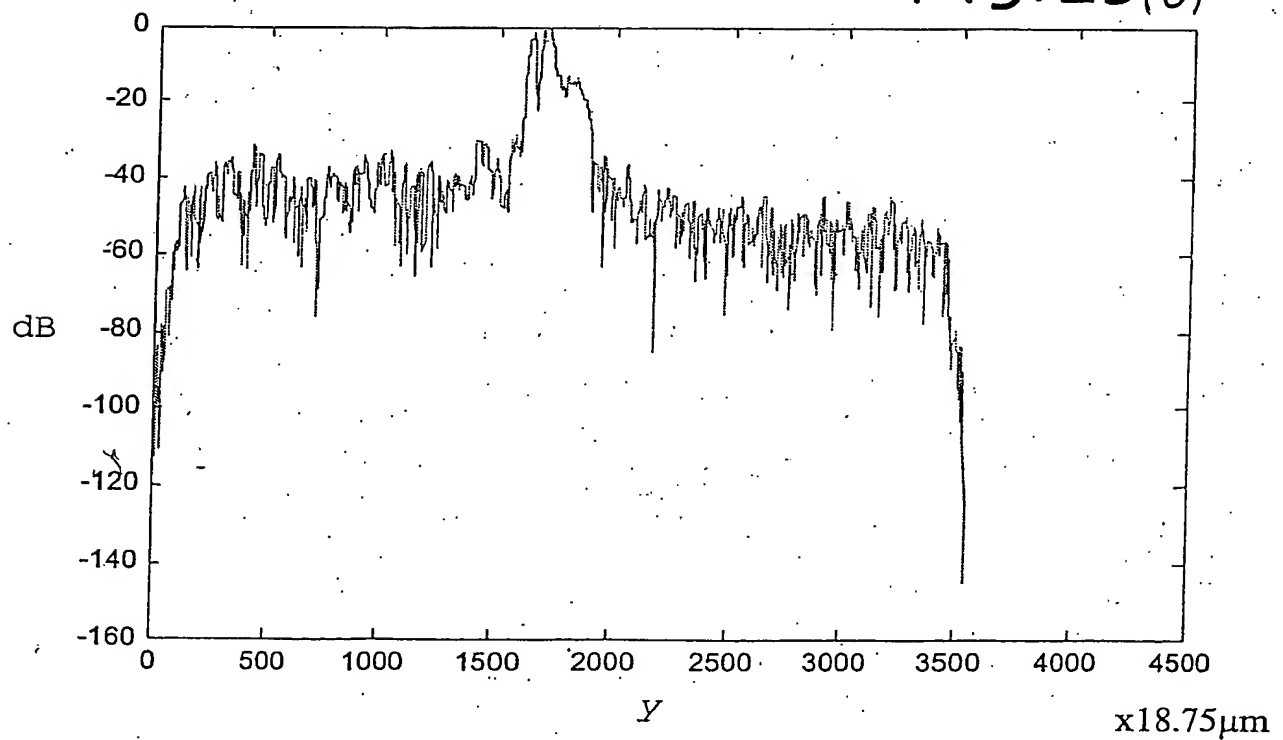
Normalized
module

Fig.29(a)



Normalized
module

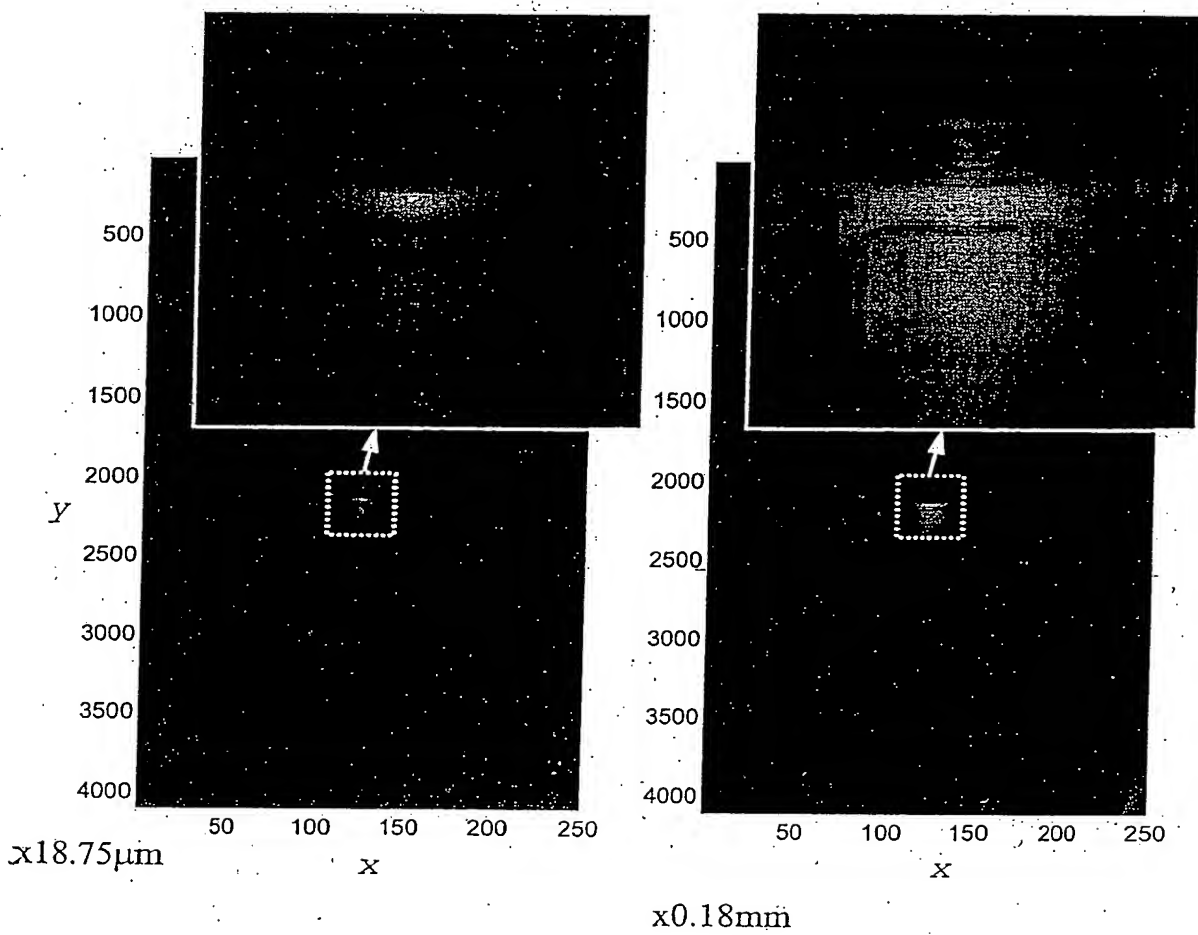
Fig.29(b)





21 / 24

Fig. 30





22 / 24

Fig. 31(a)

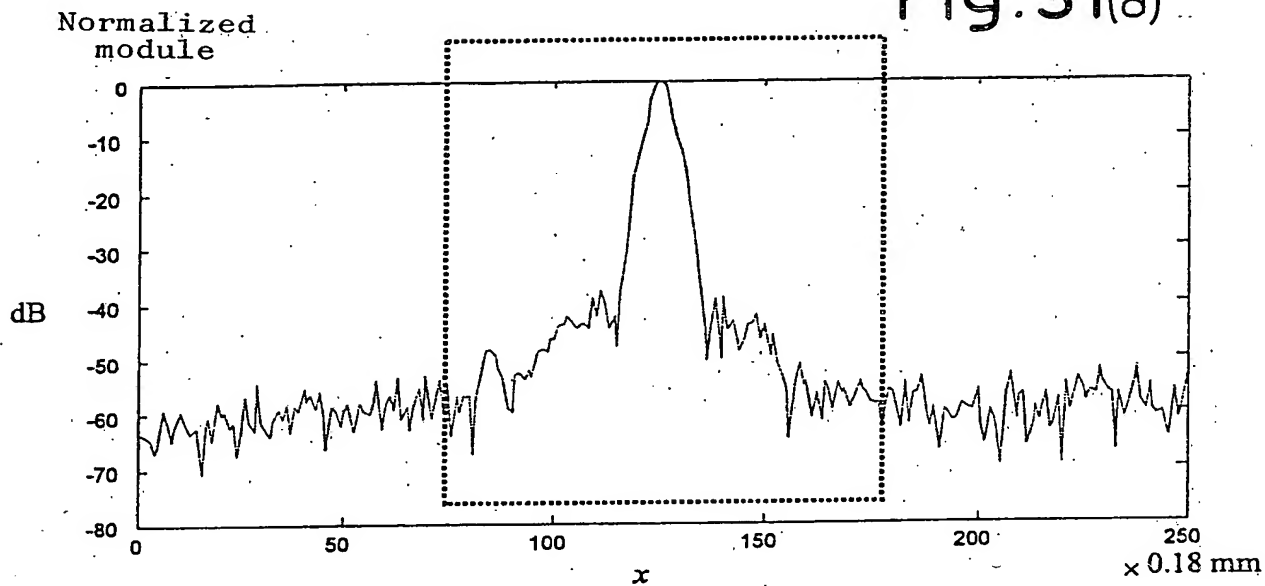
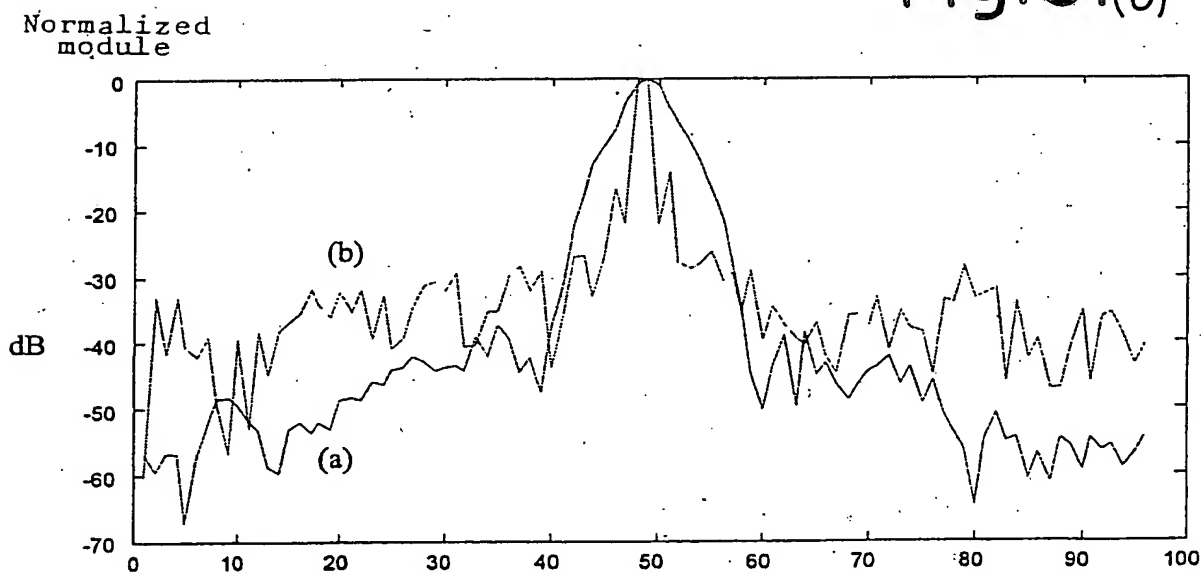


Fig. 31(b)





23 / 24

Fig.31(c)

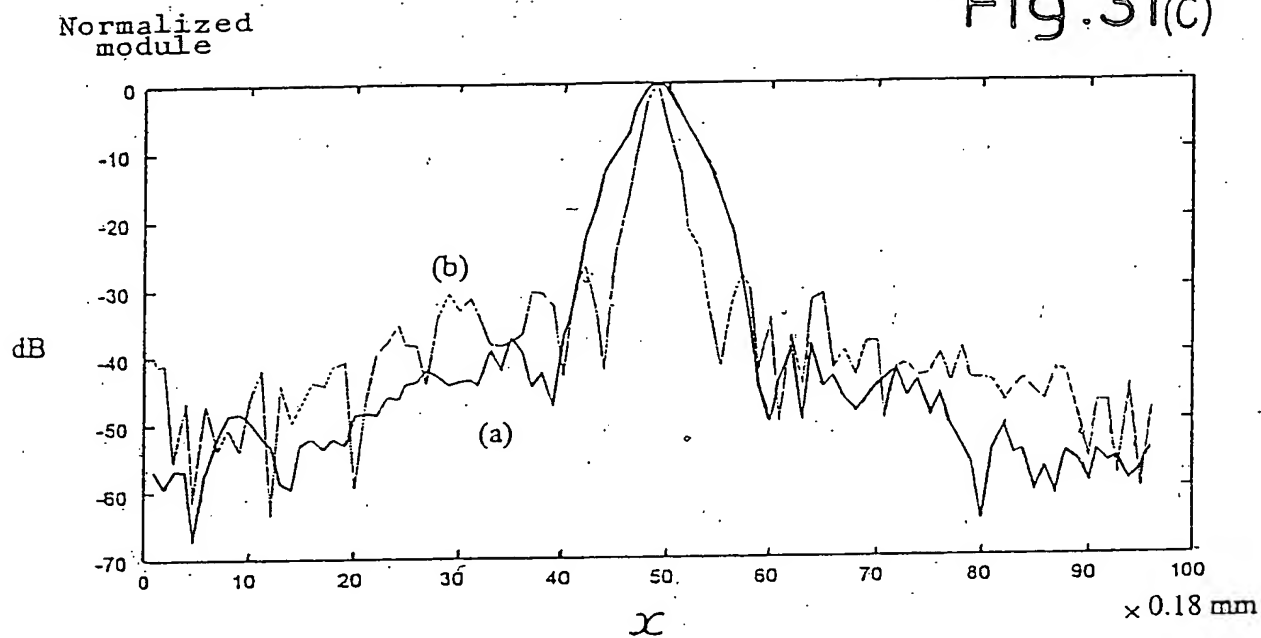
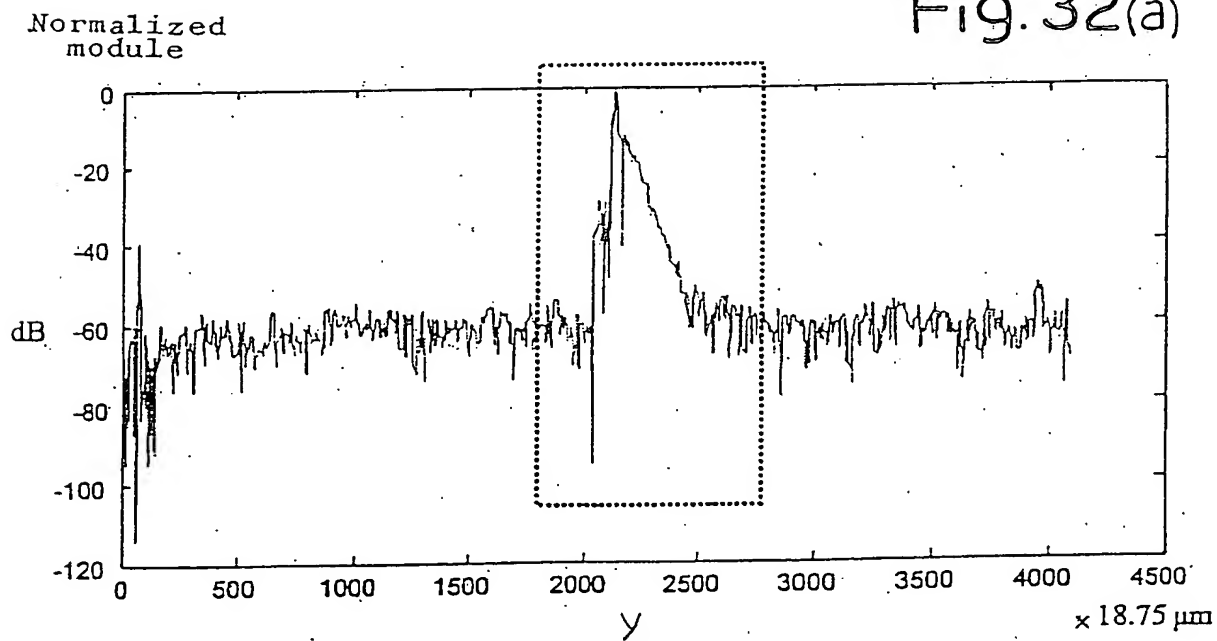


Fig.32(a)





24 / 24

Fig. 32(b)

Normalized
module

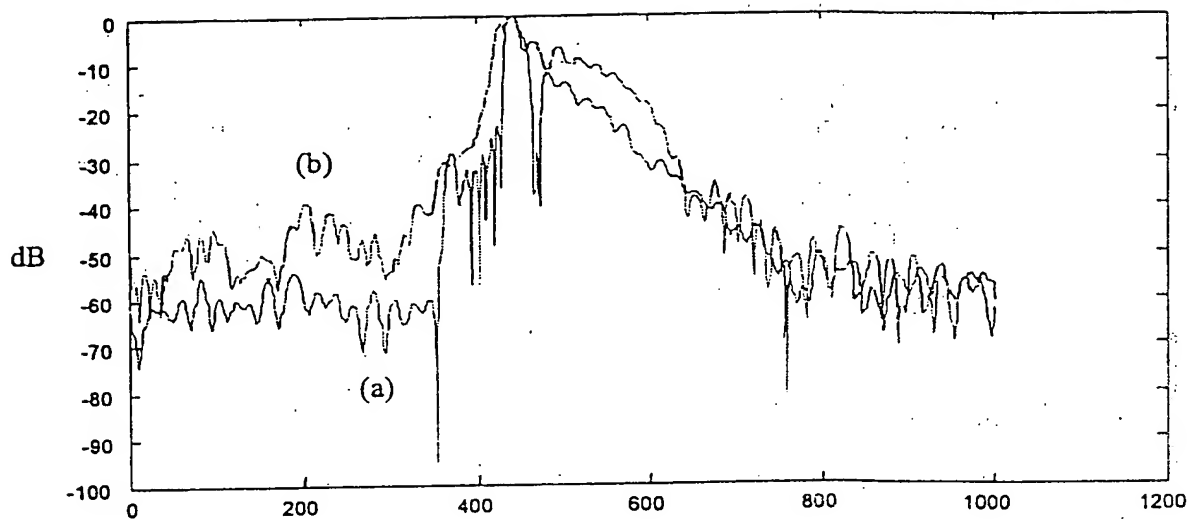


Fig. 32(c)

Normalized
module

